

MULTI-LAYER CHIP INDUCTIVE ELEMENT

ABSTRACT OF THE DISCLOSURE

A multi-layer chip inductive element includes at least two inductors connected
5 with each other and mounted in an insulating ceramic material. Each of the inductors
has a longitudinal axle parallel to the other and includes a plurality of conductor
patterns and ceramic layers stacked upon one another in sectors, wherein inductive coils
of each two adjacent inductors of the inductors are wound conversely to form sectors
thereof. Accordingly, the coils can be increased without lengthening and heightening the
10 element to further facilitate the production and enhance the yield.